



amplifier stage to produce an amplified chopped output signal.

[c23] A method in accordance with Claim 22 wherein amplifying the input signal utilizing a chopper-stabilized, silicon carbide NMOS depletion mode operational amplifier, comprises chopping the amplified chopped output signal utilizing an NMOS depletion mode amplifier responsive to a level shifted first chopping signal to produce a chopper-stabilized output signal.

[c24] A method in accordance with Claim 23 further comprising generating, a opposite nodes of a resistor of an NMOS depletion mode buffered field effect transistor logic (BFL) level shifting/inverter circuit, the first chopping signal and the level shifted first chopping signal in response to a clock signal.

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